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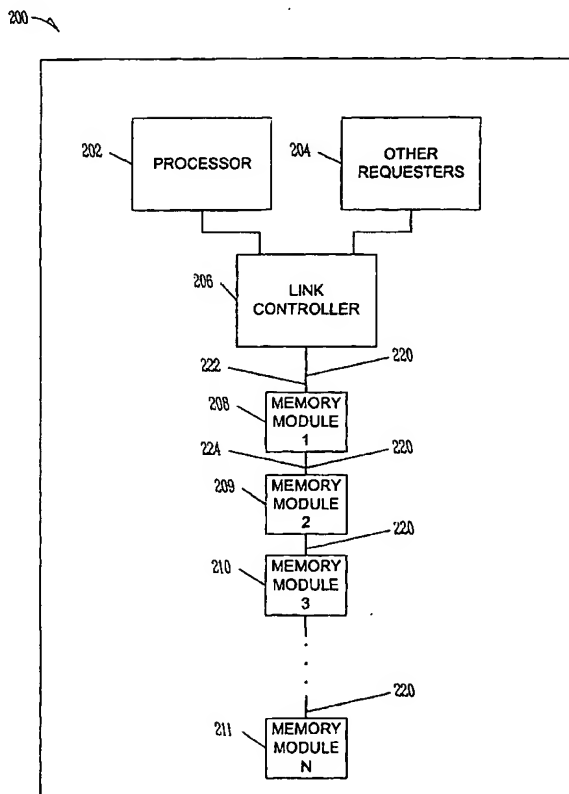
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[Continued on next page]

(54) Title: METHOD AND APPARATUS FOR SENDING DATA FROM MULTIPLE SOURCES OVER A COMMUNICATIONS
BUS



(57) Abstract: In a memory system, multiple memory mod-
ules (208-211, Figure 2) communicate over a bus (220). Each
memory module includes a hub (302, Figure 3) and at least
one memory storage unit (304). The hub receives local data
(410, Figure 4) from the memory storage units, and down-
stream data (420) from one or more other memory modules.
The hub assembles (718, Figure 7) data to be sent over the
bus within a data block structure (440), which is divided into
multiple lanes (508-515, Figure 5). An indication is made
(612, Figure 6) of where, within the data block structure,
a breakpoint will occur in the data being placed on the bus by a
first source (e.g., the local or downstream data). Based on the
indication, data from a second source (e.g., the downstream
or local data) is placed in the remainder of the data block,
thus reducing gaps on the bus.



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B. FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

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EPO-Internal, WPI Data, IBM-TDB

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	"IEEE Std 1596.4-1996 - IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)" 31 December 1996 (1996-12-31), INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, INC., NEW YORK, USA, XP002333770 ISBN: 1-55937-745-3 paragraph '1.5.1! paragraph '05.1! paragraph '6.1.3! figure B.3 figure 2	1-46
Y	WO 95/06285 A (3COM CORPORATION) 2 March 1995 (1995-03-02) pages 1,8-11 ----- -/--	1-46

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☒ Patent family members are listed in annex.

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO 02/084428 A (MICRON TECHNOLOGY, INC) 24 October 2002 (2002-10-24) page 12, line 20 - page 14, line 10 figure 1	1-46
A	----- US 2002/144085 A1 (HIRATSUKA SHINJI ET AL) 3 October 2002 (2002-10-03) paragraphs '0008!, '0031! - '0037! figure 2 -----	1-46

INTERNATIONAL SEARCH REPORT

Information on patent family members

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